

Notice of Allowability	Application No.	Applicant(s)	
	10/045,781	CHENG ET AL.	
	Examiner	Art Unit	
	Anthony Ojini	3723	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed 02/13/04.
 2. ☒ The allowed claim(s) is/are 1,2,4-12 and 14-17.
 3. ☒ The drawings filed on 12 January 2002 are accepted by the Examiner.
 4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance: **the specific limitation of** "wherein the sensors (604,606) held in the body are able to detect wafer slippage where a semiconductor wafer and a platen from which the semiconductor wafer can slip both have a substantially identical attribute" in the combination as claimed in **claims 1,9**, are not anticipated or made obvious by the prior art in the examiner's opinion. **For example, Applicant Admitted Prior Art** discloses A chemical mechanical polishing (CMP) semiconductor fabrication system comprising: a rotatable polishing pad for polishing a semiconductor wafer (308) using slurry; an oppositely rotatable platen (406) underneath the polishing pad on which the semiconductor wafer is positioned for polishing by the polishing pad; dual sensors (404,405) for detecting semiconductor wafer slippage of the semiconductor wafer from the platen, and a holder (402) to hold the dual sensors at an angle to a vertical plane, the holder having a window exposing the sensors. **However**, the art of record considered as a whole, alone or in combination fails to provide or suggest "wherein the sensors held in the body are able to detect wafer slippage where a semiconductor wafer and a platen from which the semiconductor wafer can slip both have a substantially identical attribute".

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anthony Ojini whose telephone number is 703 305 3768. The examiner can normally be reached on 7 to 4 Tuesday-Friday with every other Monday off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Joseph Hail can be reached on 703 308 2687. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

AO
May 3, 2004



Joseph J. Hail, III
Supervisory Patent Examiner
Technology Center 3700